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10/710,006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:
Anderson, et al.

Serial No.: 10/710,006

Filed: June 11, 2004

Group Art Unit: 2811

Examiner: Arena, Andrew Owens

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Frederick W. Gibb, III

For: **METHOD AND STRUCTURE FOR PROVIDING TUNED LEAKAGE CURRENT IN
CMOS INTEGRATED CIRCUIT**

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Office Action dated May 25, 2005, please consider the following: